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30593 7590 02/03/2004

HARNESS, DICKEY & PIERCE, P.L.C.
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RESTON, VA 20195

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/971,991	10/04/2001	Kyu-Nam Lim	AB-984-1C US	5338

TITLE OF INVENTION: INPUT CIRCUIT BUFFER SUPPORTING A LOW VOLTAGE INTERFACE AND A GENERAL LOW VOLTAGE TRANSISTOR LOGIC(LVVTL) INTERFACE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	05/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, LINH M	2816	327-108000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 HARNESS, DICKEY

2

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

SAMSUNG ELECTRONICS CO., LTD.

Kyungki-do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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John A. Castle/35,094

4-30-04

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